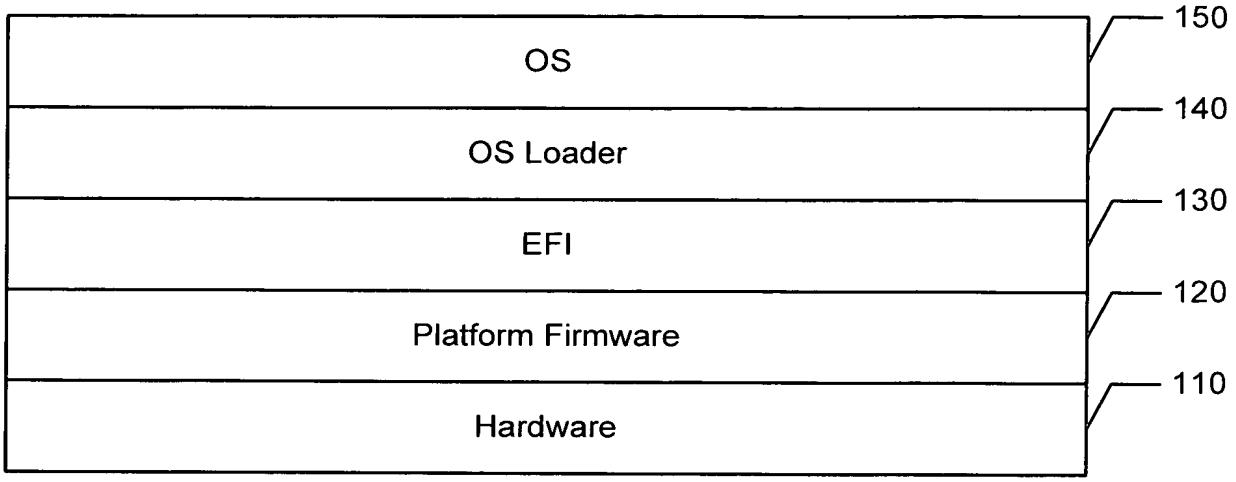
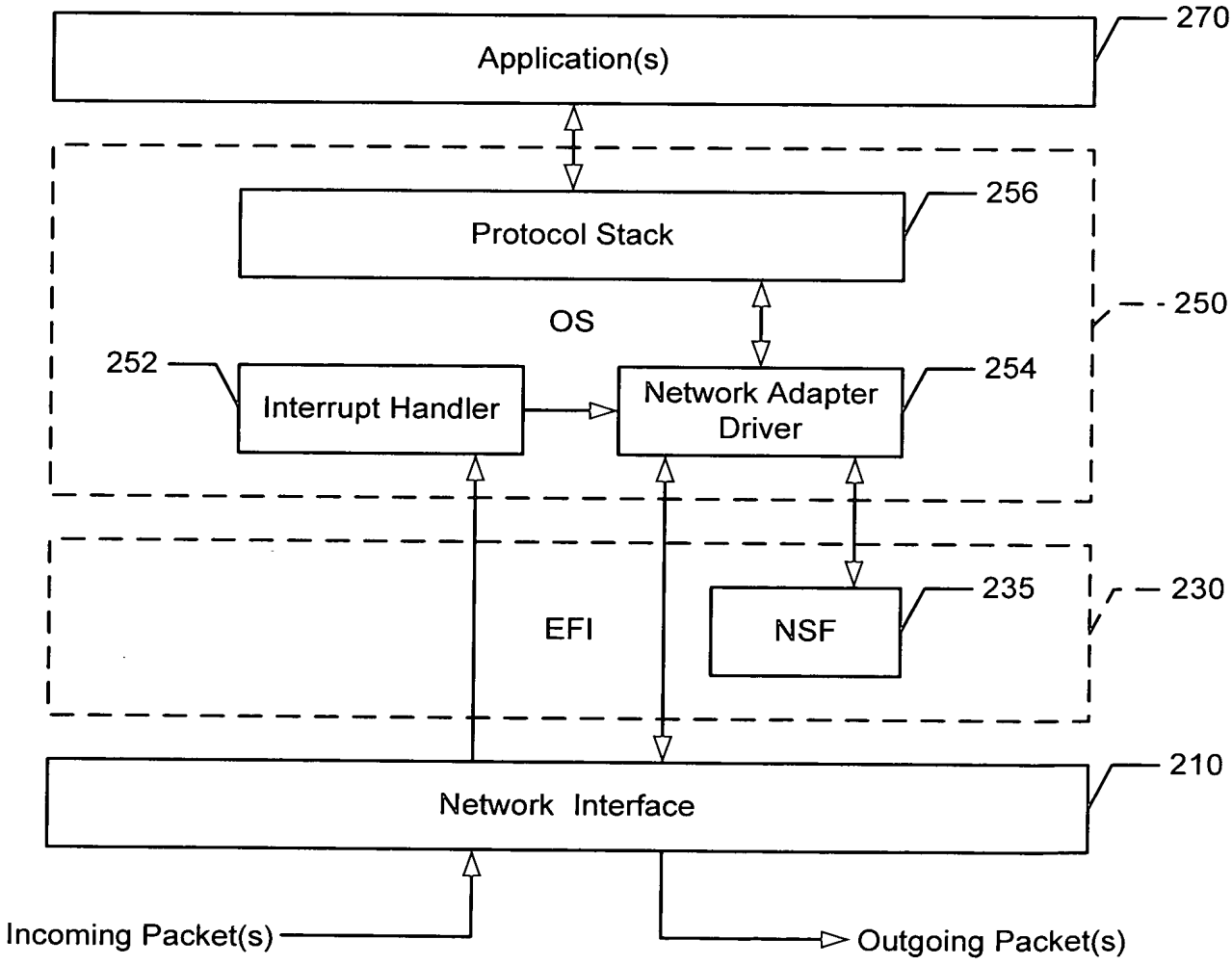


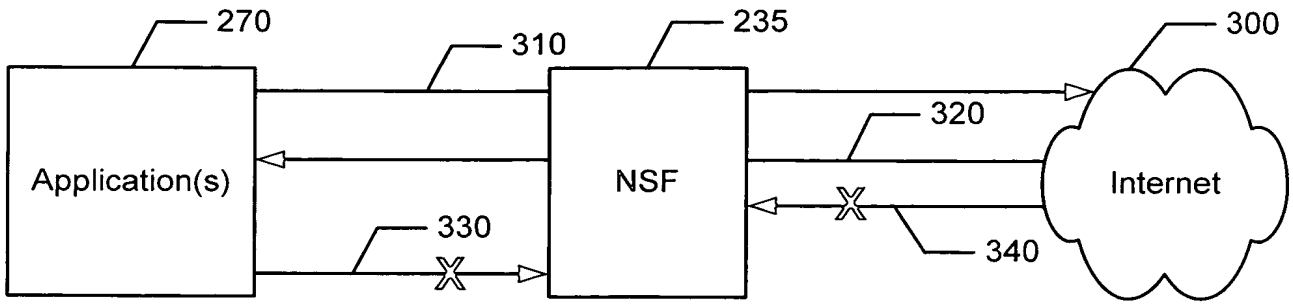
100

FIG. 1



200

FIG. 2



300

FIG. 3

```
// GUID definition
#define EFI_NETWORK_SECURITY_FIREWALL {DEADBEEF-XXXX-YYY ... ....}
// Revision Number
#define EFI_NETWORK_SECURITY_FIREWALL_REVISION 0x00010000

                                                          422
typedef struct _EFI_NETWORK_SECURITY_FIREWALL{
        UINT64 Revision;
        EFI_NETWORK_SECURITY_FIREWALL_INIT              FwInit;
        EFI_NETWORK_SECURITY_FIREWALL_DEINIT            FwDeInit;
        EFI_NETWORK_SECURITY_FIREWALL_CHECK_PKT         FwChkPkt;
        EFI_NETWORK_SECURITY_FIREWALL_ADD_RULE          FwAddRule;
432     EFI_NETWORK_SECURITY_FIREWALL_DELETE_RULE       FwDelRule;
        EFI_NETWORK_SECURITY_FIREWALL_XXXXXXX           FwXxxxxx;
        EFI_NETWORK_SECURITY_FIREWALL_YYYYYYY           FwYyyyyy;
        ... ... ....,
        EFI_NETWORK_SECURITY_FIREWALL_CONFIG_DATA       ConfigData,
        } EFI_NETWORK_SECURITY_FIREWALL;

typedef struct _EFI_NETWORK_SECURITY_FIREWALL_CONFIG_DATA {
        UINT32 Rule ID;
        UINT32 SourceIPAddress;
        UINT32 DestinationIPAddress;
        ... ... .....
        } EFI_NETWORK_SECURITY_FIREWALL_CONFIG_DATA;
// define function pointers
EFI_STATUS
(EFIAPI * EFI_NETWORK_FIREWALL_INIT) (
    IN EFI_NETWORK_SECURITY_FIREWALL_CONFIG_DATA InitData
    );

EFI_STATUS
(EFIAPI * EFI_NETWORK_FIREWALL_INIT) (
    VOID
    );
```

400

FIG. 4

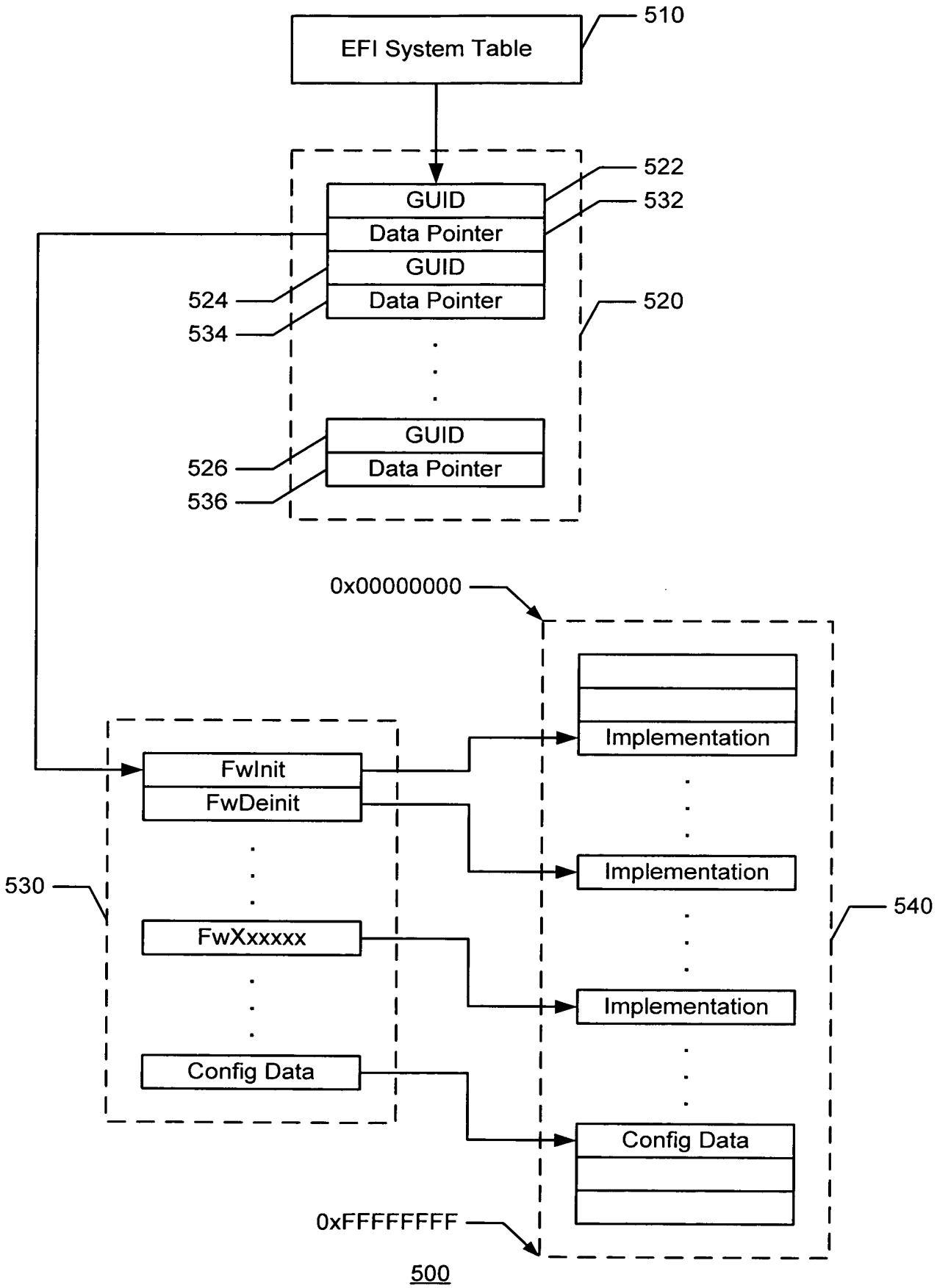
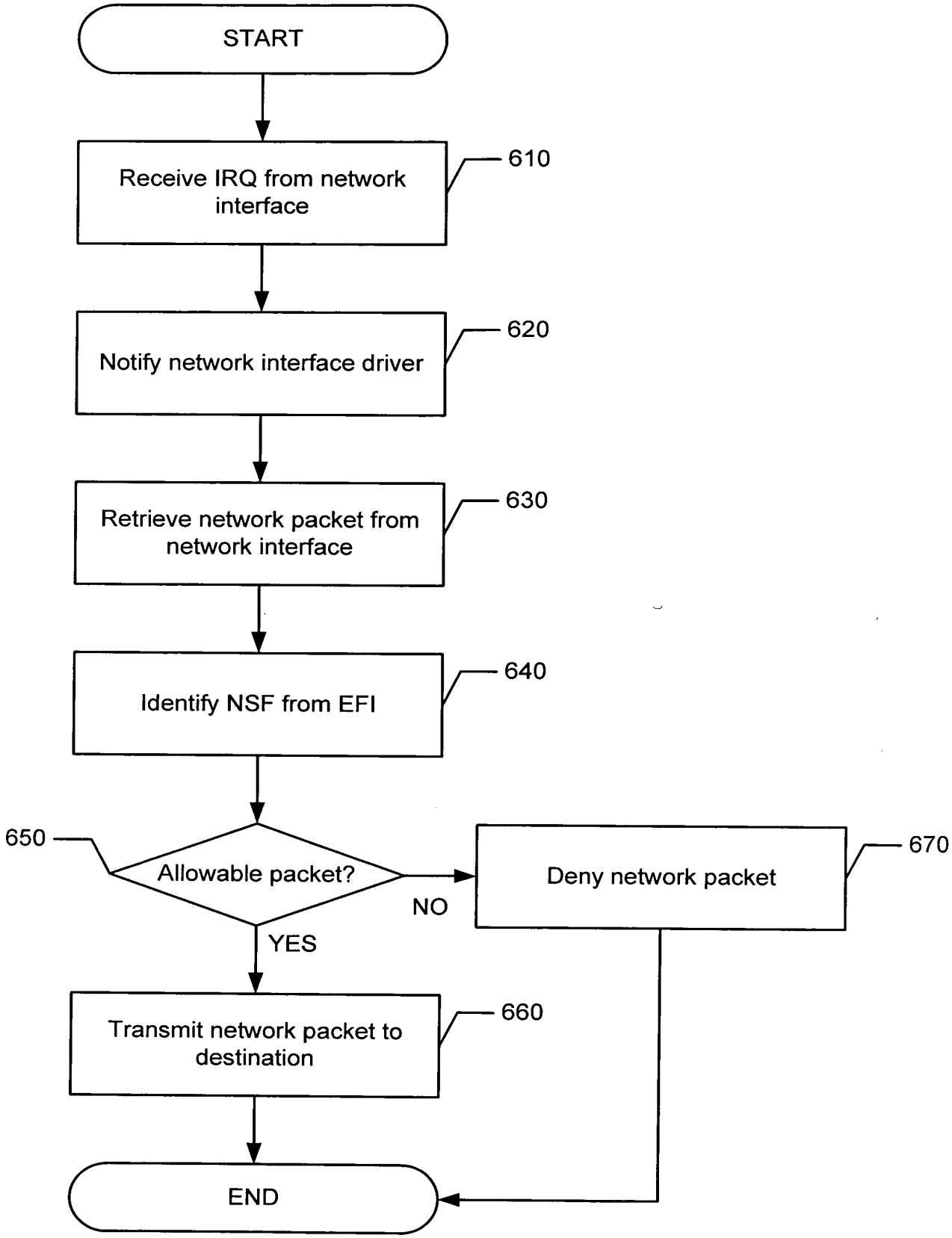
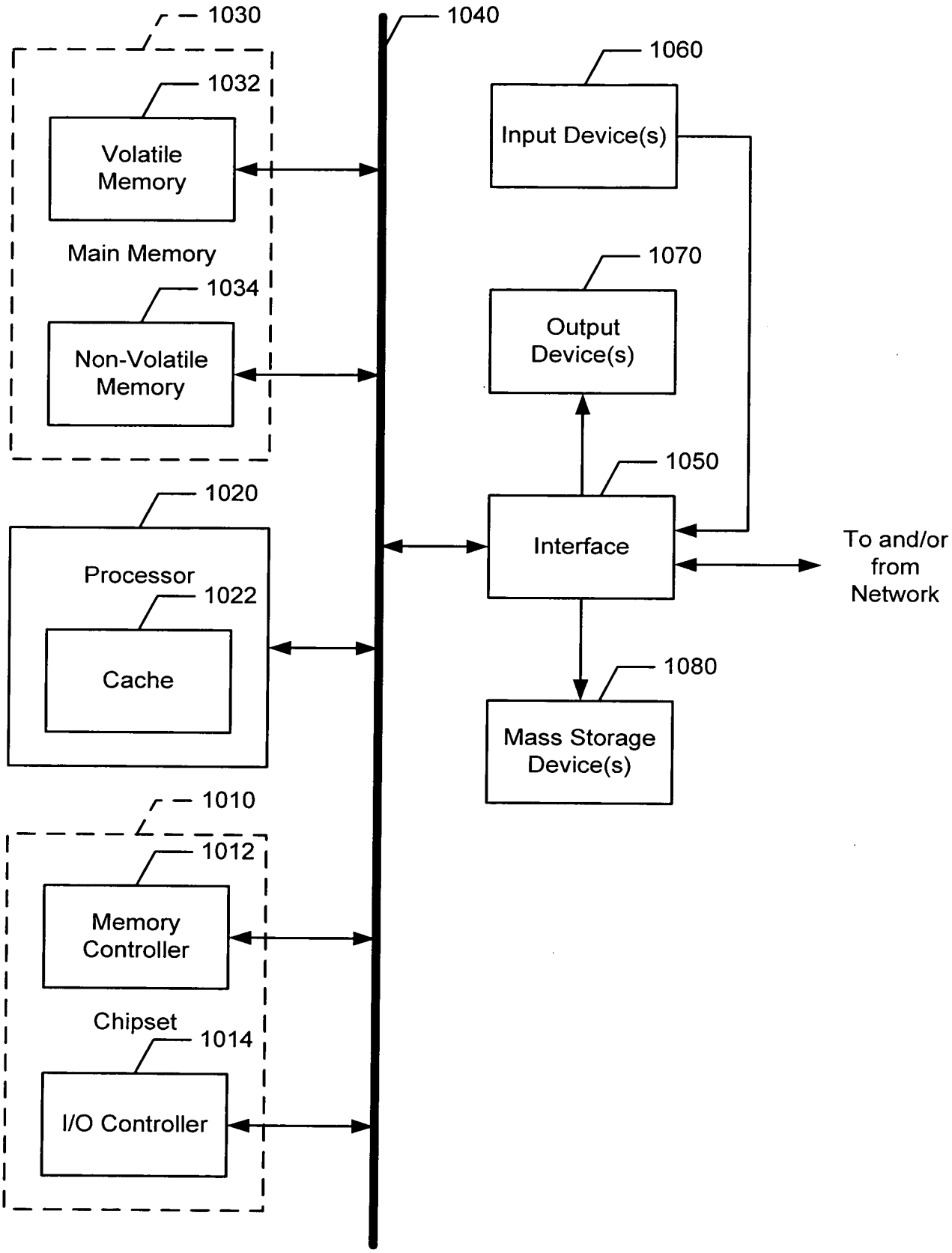

EFI System Table
510
GUID
Data Pointer
GUID
Data Pointer
522
532
524
534
520
GUID
Data Pointer
526
536
0x00000000
Implementation
Implementation
Implementation
Config Data
FwInit
FwDeinit
FwXxxxxx
Config Data
530
540
0xFFFFFFFF
500

FIG. 5


START
Receive IRQ from network interface
610
Notify network interface driver
620
Retrieve network packet from network interface
630
Identify NSF from EFI
640
650
Allowable packet?
NO
Deny network packet
670
YES
Transmit network packet to destination
660
END

600

FIG. 6


1030
1040
1060
1032
Volatile Memory
Input Device(s)
Main Memory
1070
1034
Output Device(s)
Non-Volatile Memory
1050
1020
Interface
To and/or from Network
Processor
1022
Cache
1080
Mass Storage Device(s)
1010
1012
Memory Controller
Chipset
1014
I/O Controller

1000

FIG. 7